

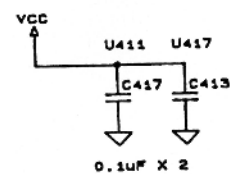
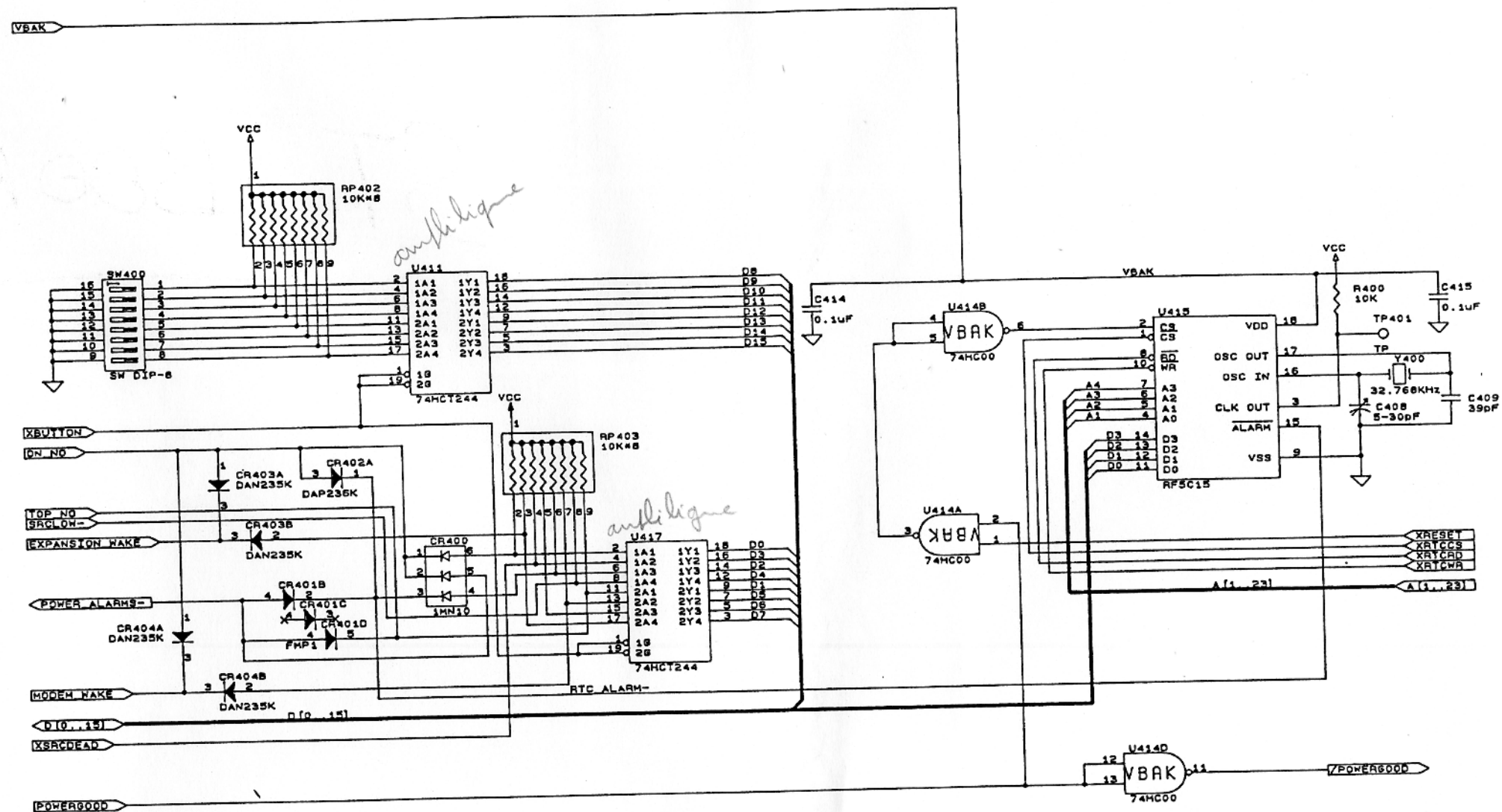
ST BOOK

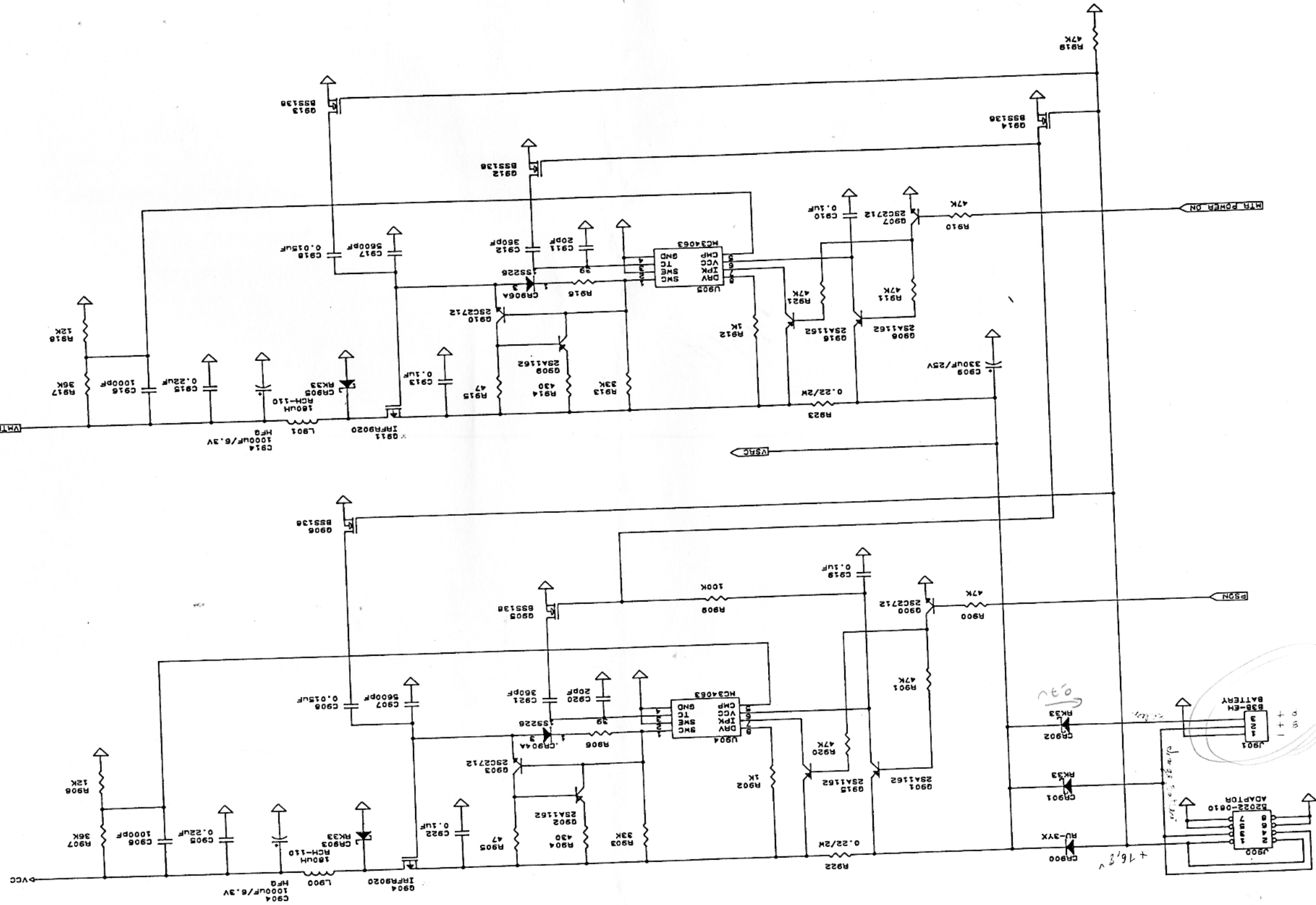
REV	REVISIONS DESCRIPTION	DATE	APPROVED
1	PRELIMINARY RELEASE		
6	1MB/4MB VERSION	91.09.30	
7	ENGINEERING RELEASE		

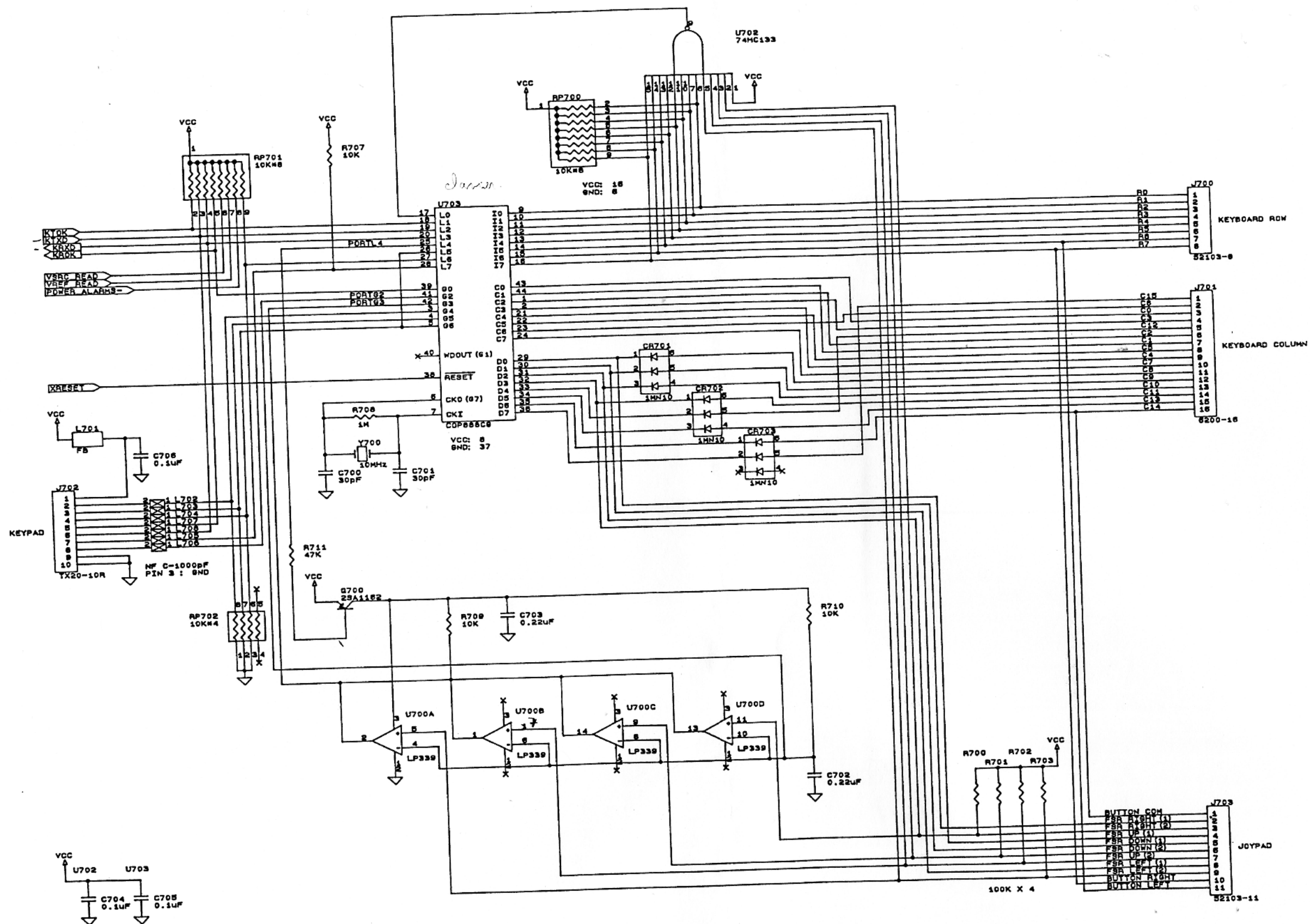
MAXI ST SCHEMATIC DIAGRAMS
92.03.18

CPU, ROM & RESET SHEET 2 OF 12 MAXI_ST1.SCH	SERIAL & PARALLEL SHEET 3 OF 12 MAXI_ST2.SCH	MCU & LCD CONTROL SHEET 4 OF 12 MAXI_ST3.SCH
RAM SHEET 5 OF 12 MAXI_ST4.SCH	REFRESH CONTROL SHEET 6 OF 12 MAXI_ST5.SCH	ACSI & IDE I/F SHEET 7 OF 12 MAXI_ST6.SCH
KBDD CONTROL SHEET 8 OF 12 MAXI_ST7.SCH	POWER SYSTEM SHEET 9 OF 12 MAXI_ST8.SCH	DC-DC CONVERTER SHEET 10 OF 12 MAXI_ST9.SCH
ALARMS & RTC SHEET 11 OF 12 MAXI_STA.SCH	UNUSED BATES SHEET 12 OF 12 MAXI_STB.SCH	

DRAWN BY J. TITTLER		DATE 09/30/91	ATARI CORPORATION	
CHECKED		DATE	TITLE SCHEMATIC DIAGRAM MAXIST	
ENGINEER		DATE	SIZE C	
A. SHINAGAWA		DATE 08/08/91	DRAWING NO. C104446-001	
APPROVED		DATE	REV 7.0	
CA200691-XXX NEXT ASSY APPLICATION		MAXIST USED ON ORIGINAL MODEL		SHEET 1 OF 12







ATARI CORPORATION			
Title			
SCHEMATIC DIAGRAM MAXIST			
Size	Document Number	REV	
C	C104448-001	7.0	
Date:	August 14, 1982 Ernest 8 of 12		

