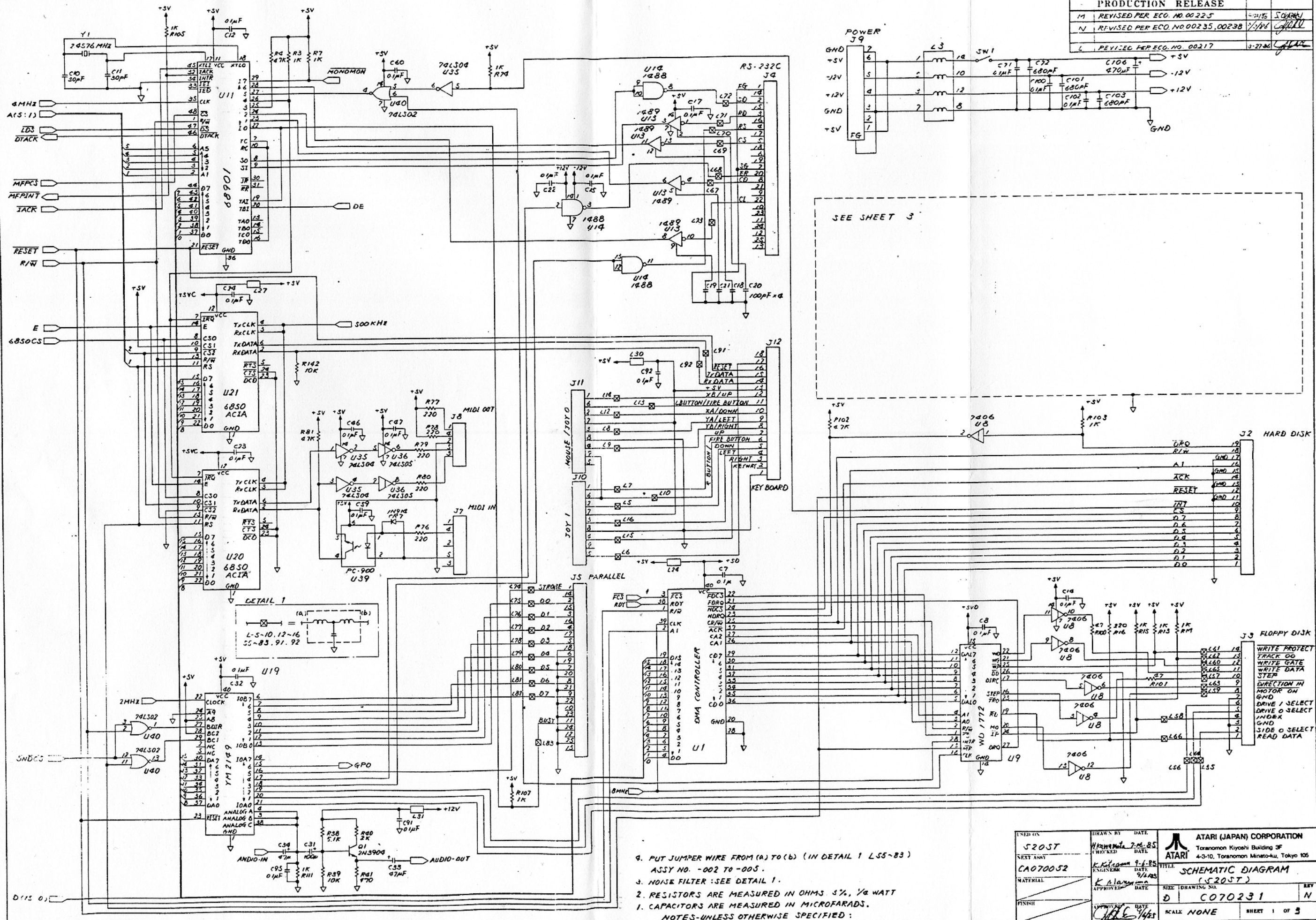


REV	REVISIONS DESCRIPTION	DATE	APPROVED
	PRODUCTION RELEASE		
1A	REVISED PER ECO. NO. 00225	2-21-86	S. GARDNER
N	REVISED PER ECO. NO. 00235, 00238	3-19-86	JOHN
L	REVISED PER ECO. NO. 00217	3-27-86	CLARK



4. PUT JUMPER WIRE FROM (A) TO (B) (IN DETAIL 1 L55-83 ASSY NO. -002 TO -005.

3. NOISE FILTER :SEE DETAIL 1.

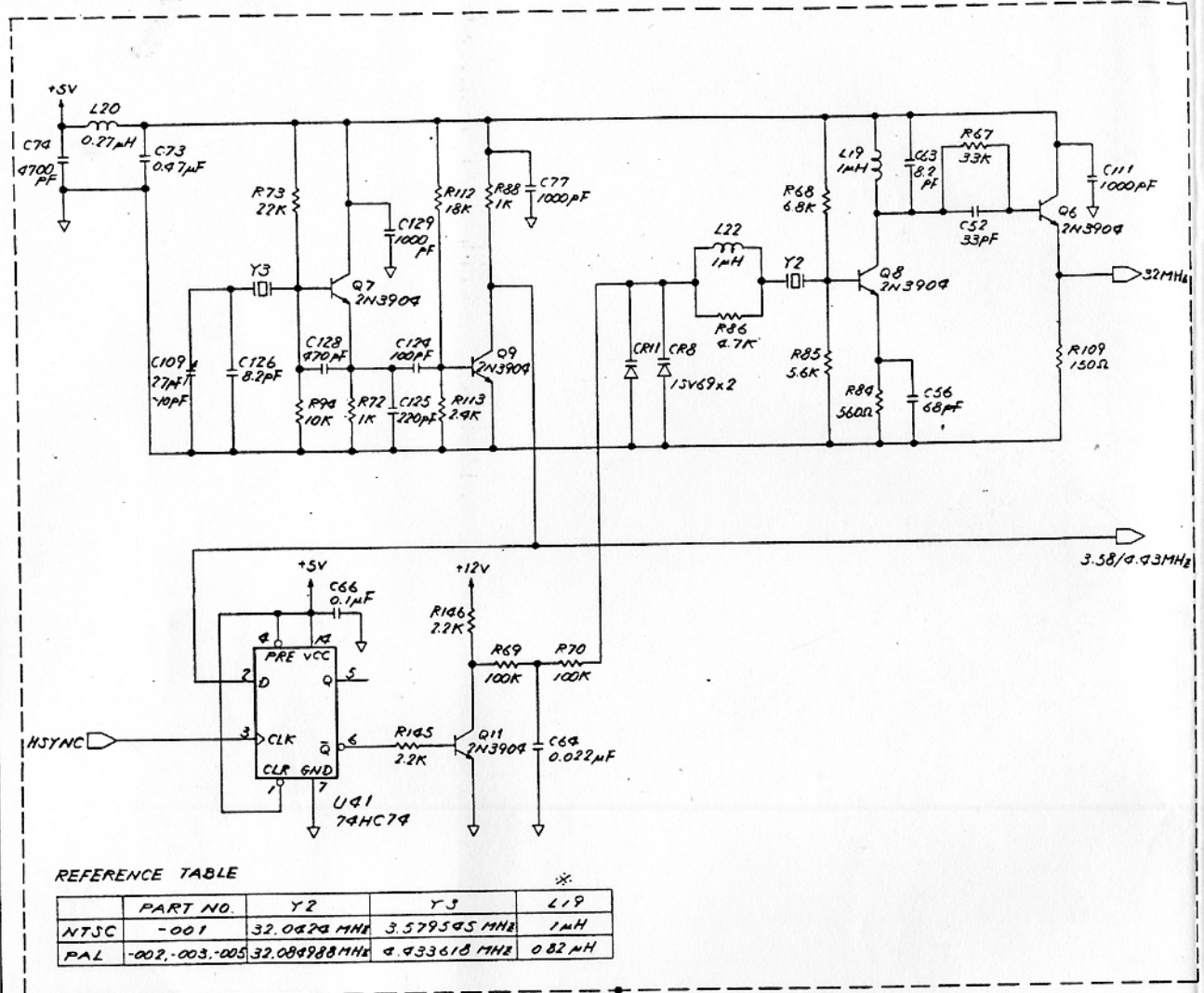
2. RESISTORS ARE MEASURED IN OHMS 5%, 1/4 WATT

1. CAPACITORS ARE MEASURED IN MICROFARADS.

NOTES-UNLESS OTHERWISE SPECIFIED :

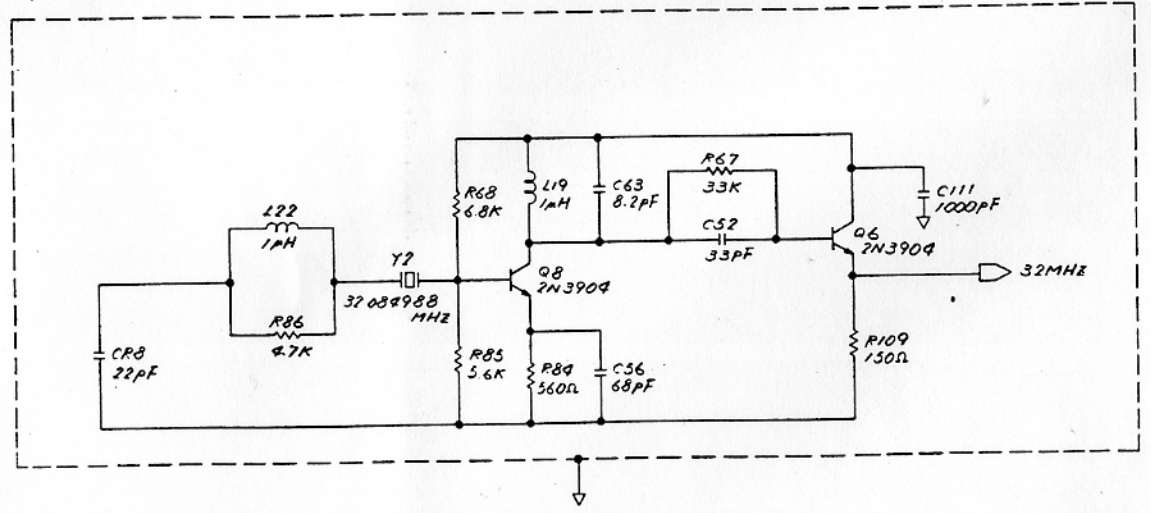
* SEE SHEET 3





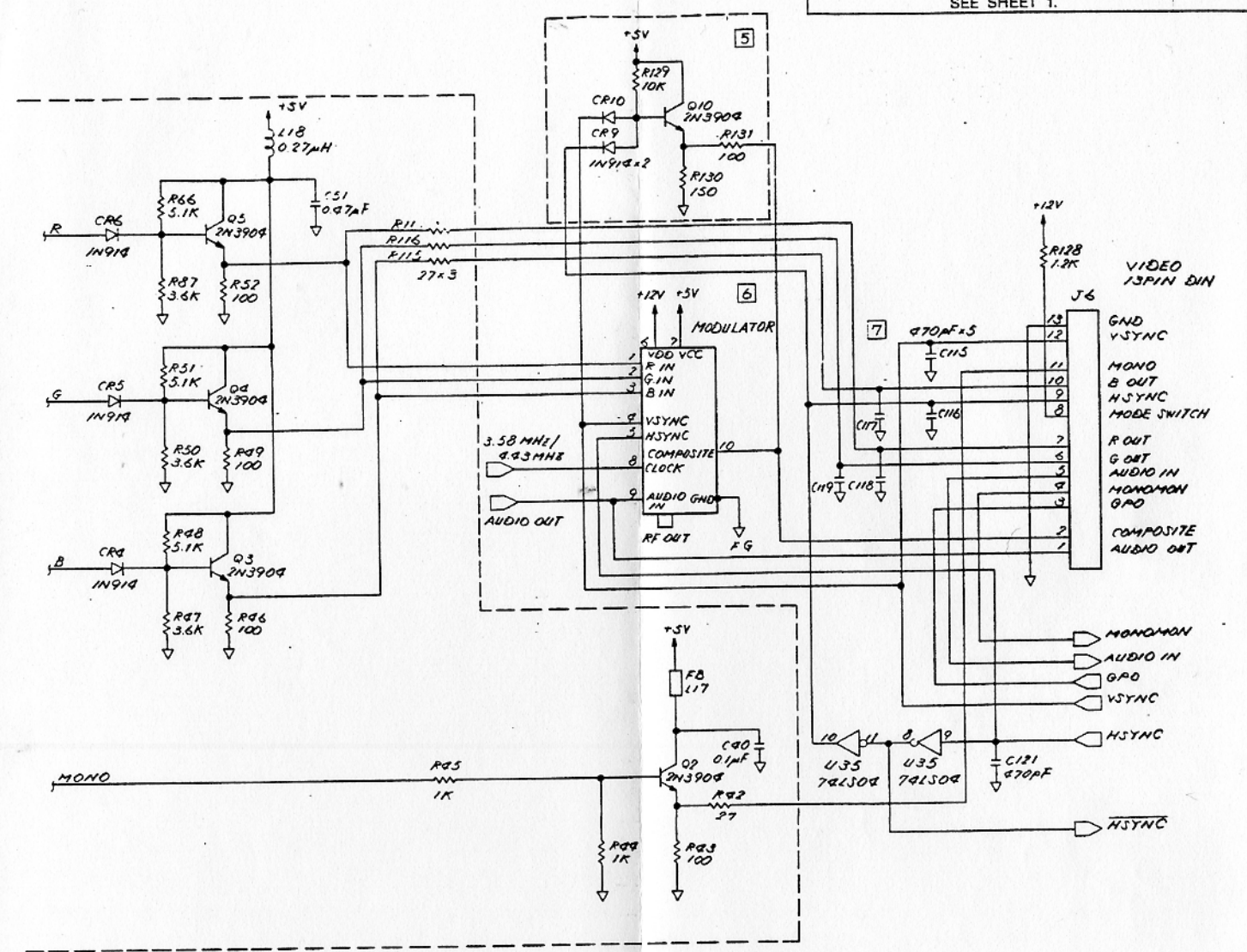
REFERENCE TABLE

	PART NO.	Y2	Y3	Y4
NTSC	-001	32.0629 MHz	3.579545 MHz	1 MHz
PAL	-002, -003, -005	32.084988 MHz	4.433618 MHz	0.82 MHz



REFERENCE TABLE

	PART NO.	Y2
PAL	CA070052-004	32.084988 MHz



REFERENCE TABLE

ASSY NO.	[6] MODULATOR	[7] CAP: C115-C119	[5] COMPOSITE SYNC
CA070052-001	C070517	REQUIRED	NOT REQUIRED
-002	C070518	NOT REQUIRED	NOT REQUIRED
-003	C070519	REQUIRED	NOT REQUIRED
-004	NOT REQUIRED	NOT REQUIRED	REQUIRED
CA070052-005	C070519	NOT REQUIRED	NOT REQUIRED

DESIGNED BY H. Kawanishi	DRAWN BY H. Kawanishi	DATE 9-28-86	ATARI (JAPAN) CORPORATION Toranomon Kiyoshi Building 3F 4-3-10, Toranomon Minato-ku, Tokyo 105
CHECKED BY K. Kawanishi	ENGINEER K. Kawanishi	DATE 5-28-86	
MATERIAL	APPROVED BY K. Kawanishi	DATE 5-28-86	
FINISH	DATE 9/28/86	SCALE NONE	
TITLE SCHEMATIC DIAGRAM (32052)			SIZE D
DRAWING NO. C070231			REV N
SHEET 3 OF 3			

