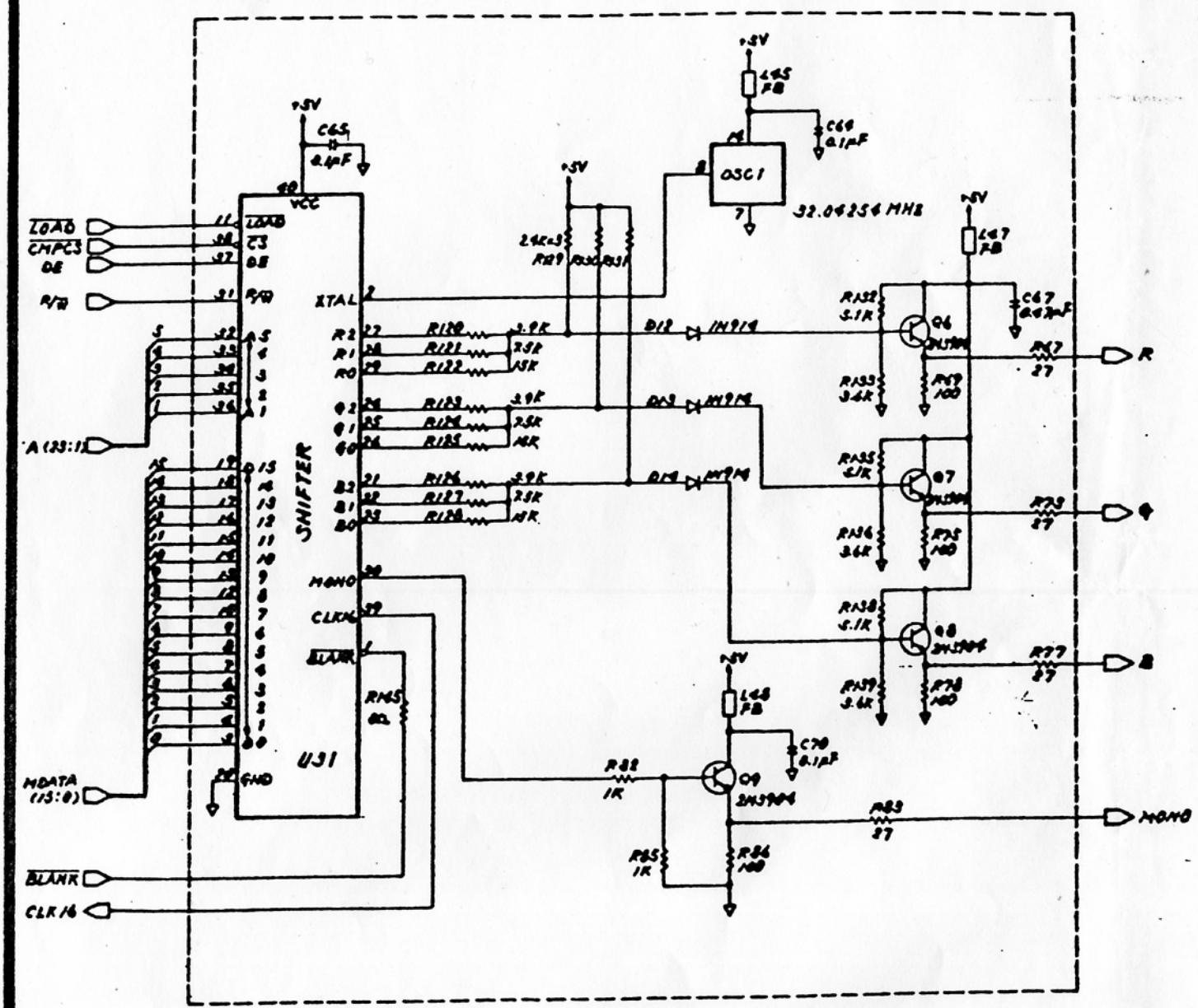


[S] ROM TABLE
 IC ; U12 (74LS11)
 JUMPER ; W2, W3, W4

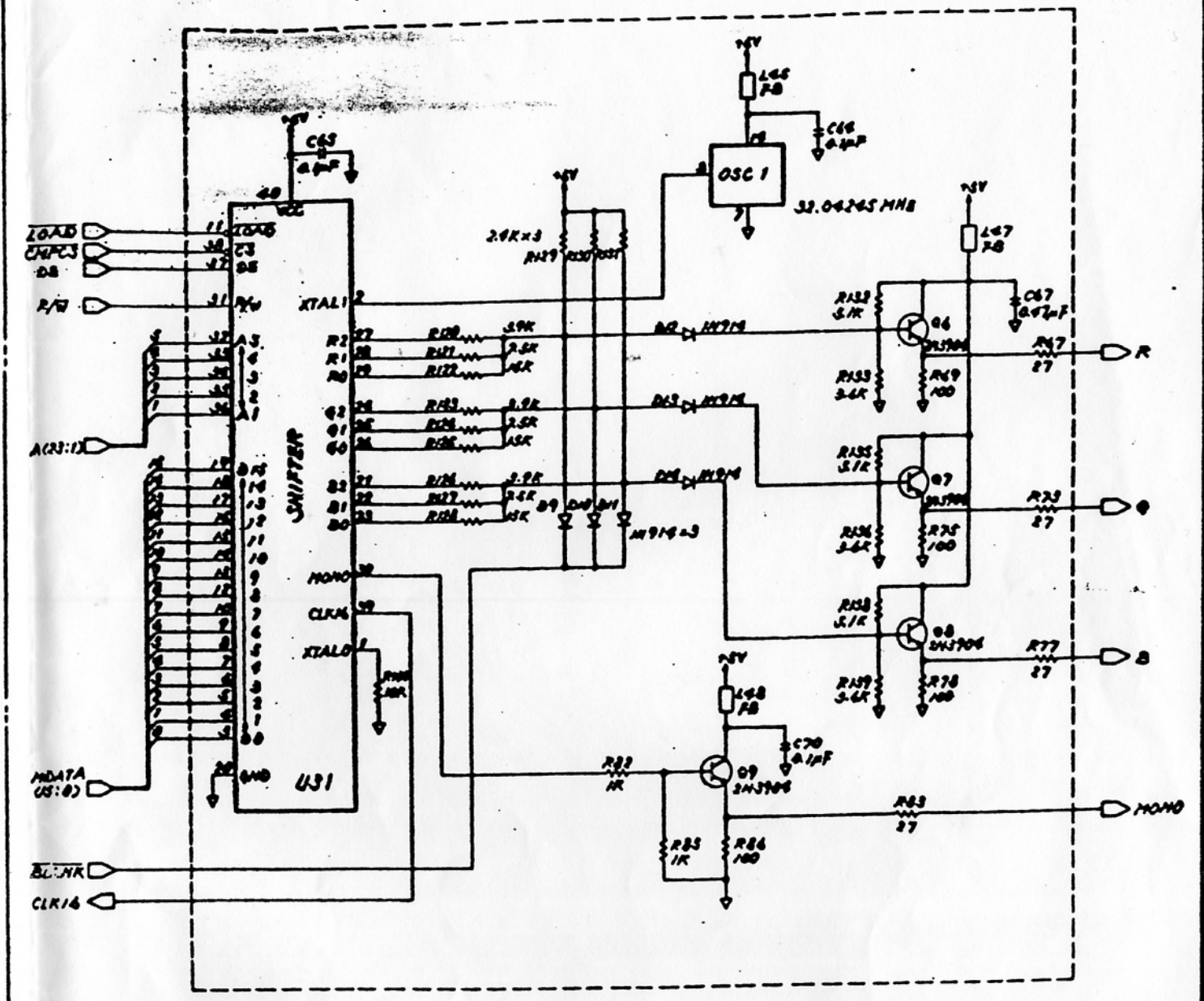
ROM	LOCATION			
	W2	W3	W4	U12
256K	1-2	1-2	JUMPER	NO USE
1M x 2	2-3	2-3	NOH	USE
1M x 6	2-3	2-3	JUMPER	NO USE

NOTES - UNLESS OTHERWISE SPECIFIED :

USED ON	DRAWN BY	DATE	ATARI (JAPAN) CORPORATION Toranomon Kiyoshi Building 3F 4-3-10, Toranomon Minato-ku, Tokyo 106
NEXT ASSY	CHECKED	DATE	
MATERIAL	ENGINEER	DATE	TITLE
FINISH	APPROVED	DATE	SCHEMATIC DIAGRAM (MEGAST 4)
	APPROVED	DATE	SIZE C DRAWING NO. C100186
	APPROVED	DATE	SCALE NONE SHEET 5 OF 6



PIN C070713 FULL CUSTOM w/o D TO A CONVERTER



PIN C025914 GATE ARRAY w/o D TO A CONVERTER

